FIG.1

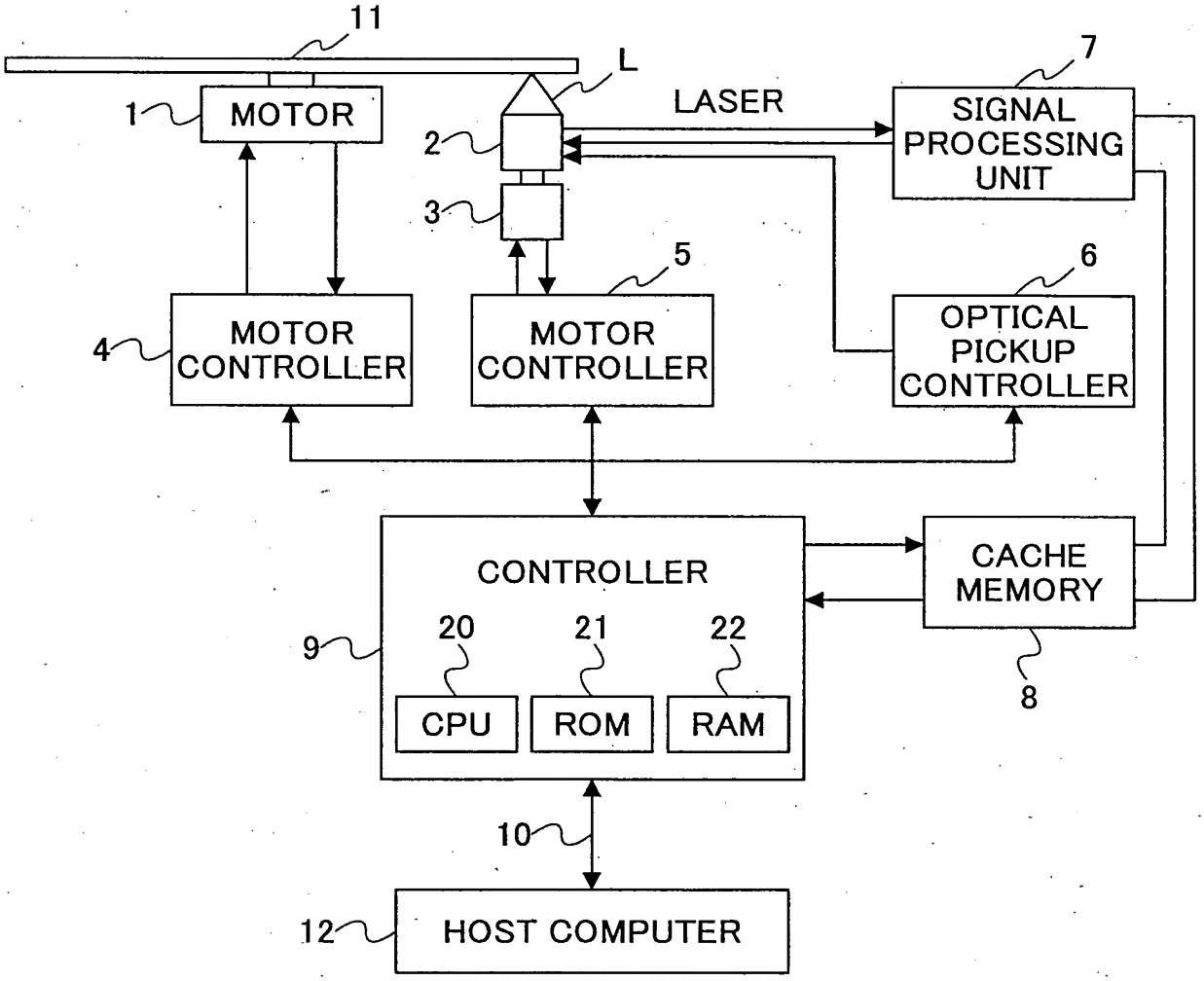

11
L
7
1
MOTOR
LASER
SIGNAL PROCESSING UNIT
2
3
5
6
4
MOTOR CONTROLLER
MOTOR CONTROLLER
OPTICAL PICKUP CONTROLLER
CONTROLLER
CACHE MEMORY
9
20
21
22
CPU
ROM
RAM
8
10
12
HOST COMPUTER

FIG.2

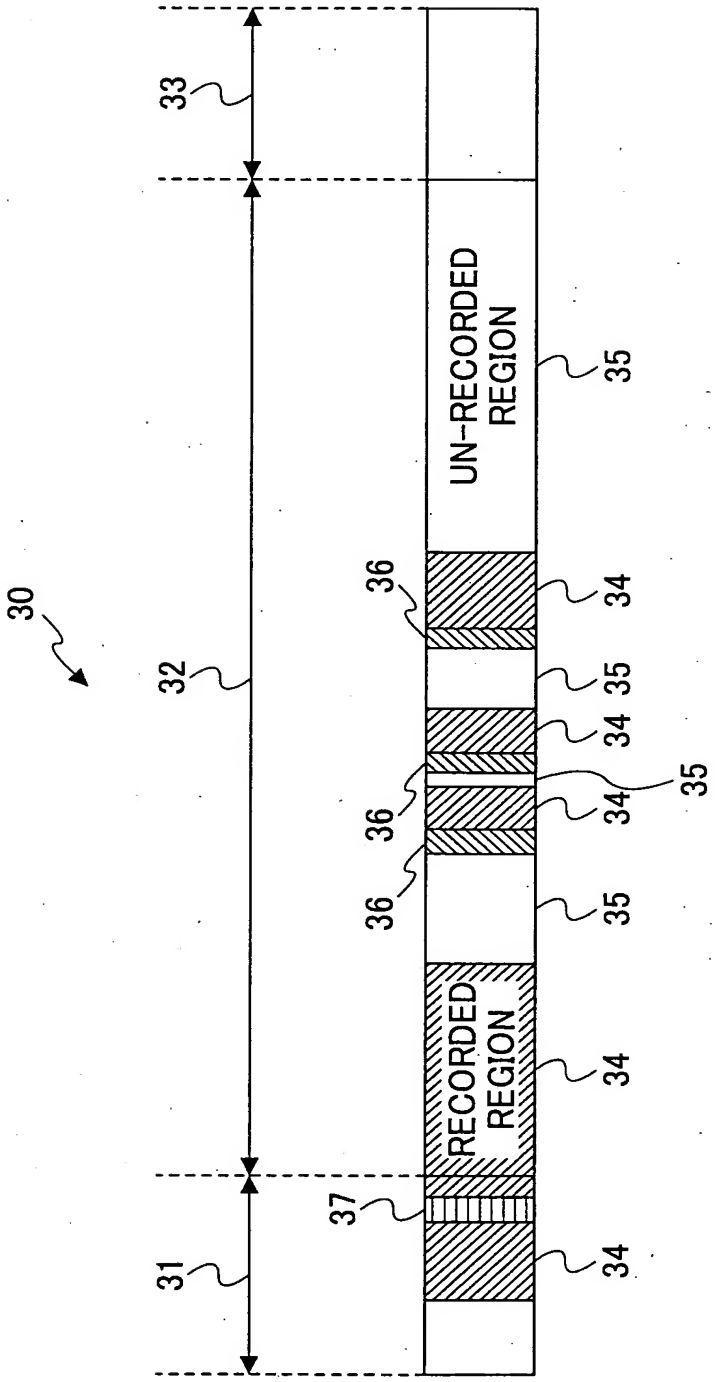


FIG.3

PHYSICAL SECTOR OF ECC BLOCK	MAIN DATA BYTE POSITION	DESCRIPTION	NUMBER OF BYTES
0	D0 to D3	CONTENTS DESCRIPTOR	4
0	D4 to D7	UNKNOWN CONTENTS DESCRIPTOR ACTIONS	4
0	D8 to D39	DRIVE ID	32
0	D40 to D43	FDCB UPDATE COUNT	4
0	D44 to D47	FORMATTING STATUS AND MODE	4
0	D48 to D51	LAST WRITTEN ADDRESS	4
0	D52 to D55	LAST VERIFIED ADDRESS	4
0	D56 to D59	BITMAP START ADDRESS	4
0	D60 to D63	BITMAP LENGTH	4
0	D64 to D95	DISC ID	32
0	D96 to D127	APPLICATION DEPENDENT	32
0	D128 to D2048	RESERVED AND SET TO (00)	1920
1 to 9	D0 to D2048	FORMATTING BITMAP	9 × 2048
10 to 15	D0 to D2048	RESERVED AND SET TO (00)	6 × 2048

App No.: Not Yet Assigned Docket No.: R2184.0266/P266
Inventor: Yoshiyuki Sasaki
Title: DATA RECORDING DEVICE, DATA RECORDING METHOD, PROGRAM AND DATA RECORDING MEDIUM
Sheet 3 of 6

FIG.4

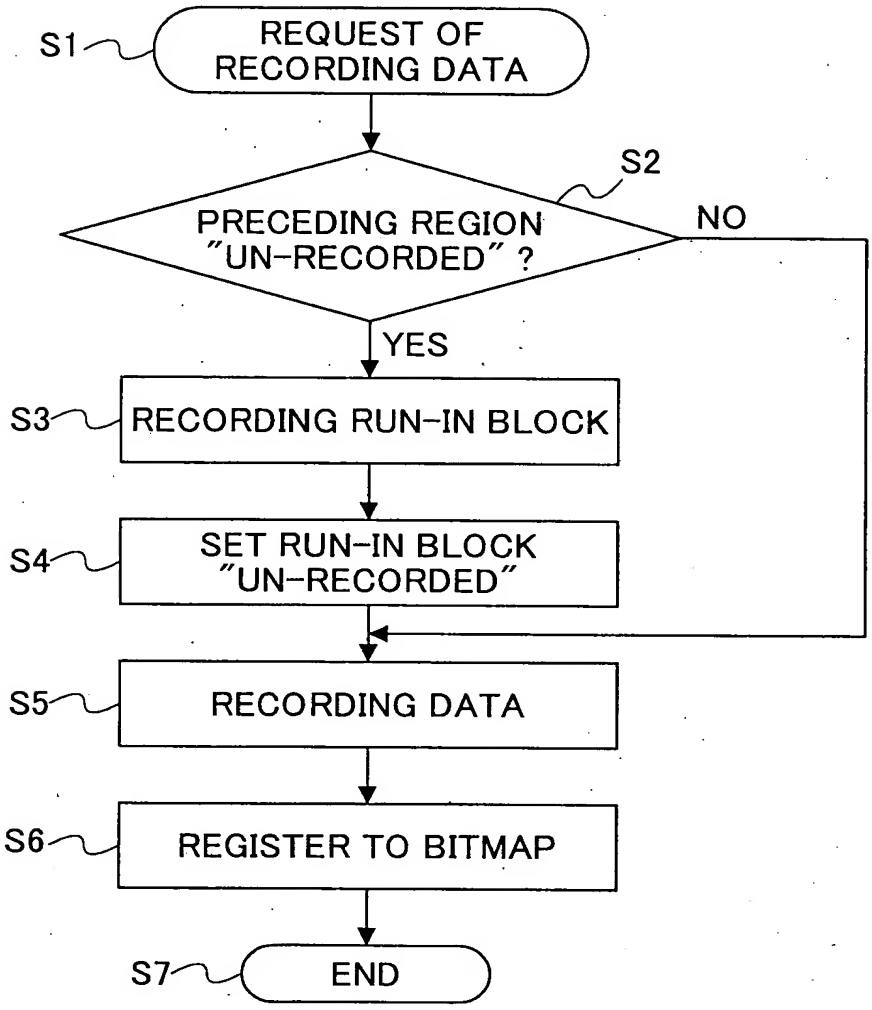

S1
REQUEST OF RECORDING DATA
S2
PRECEDING REGION "UN-RECORDED" ?
NO
YES
S3
RECORDING RUN-IN BLOCK
S4
SET RUN-IN BLOCK "UN-RECORDED"
S5
RECORDING DATA
S6
REGISTER TO BITMAP
S7
END

FIG.5

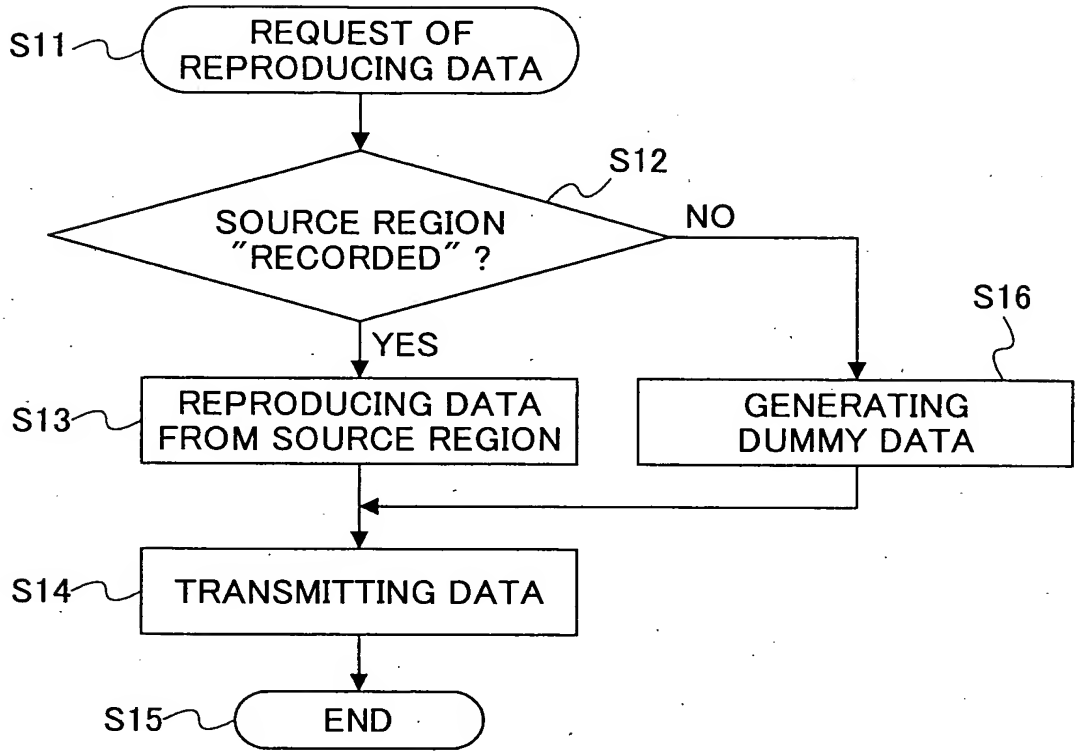
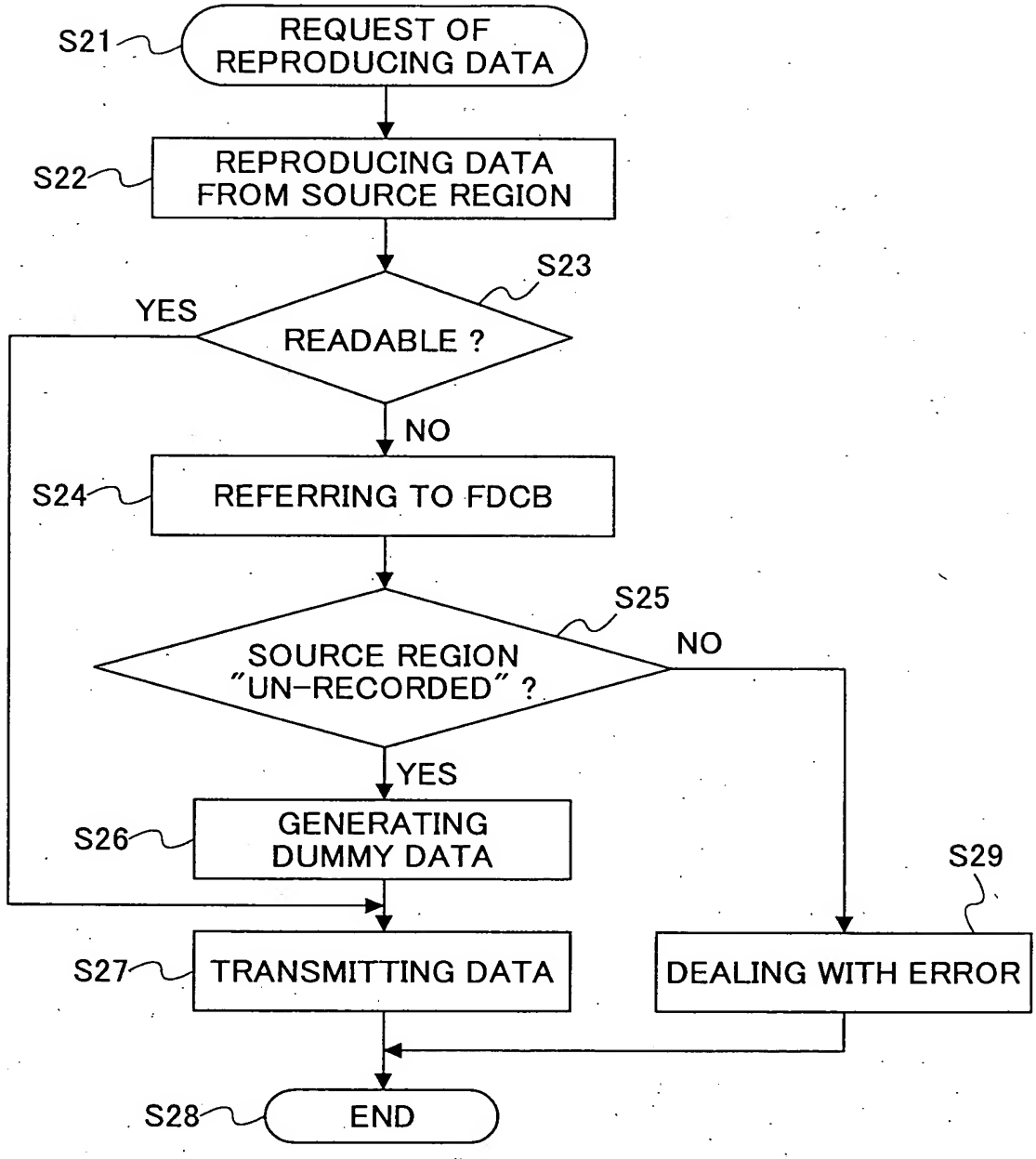

S11
REQUEST OF REPRODUCING DATA
S12
SOURCE REGION "RECORDED" ?
NO
YES
S16
S13
REPRODUCING DATA FROM SOURCE REGION
GENERATING DUMMY DATA
S14
TRANSMITTING DATA
S15
END

FIG.6


S21
REQUEST OF REPRODUCING DATA
S22
REPRODUCING DATA FROM SOURCE REGION
S23
READABLE ?
YES
NO
S24
REFERRING TO FDCB
S25
SOURCE REGION "UN-RECORDED" ?
NO
YES
S26
GENERATING DUMMY DATA
S27
TRANSMITTING DATA
S29
DEALING WITH ERROR
S28
END